



Regulation R17

Subject code: 1P4DD

**TKR COLLEGE OF ENGINEERING AND TECHNOLOGY**

(Autonomous, Accredited by NAAC with 'A' Grade)

**B.Tech II Year II Semester Supplementary Examinations, September 2023**  
**ANALOG COMMUNICATIONS**

(ECE)

Maximum Marks: 70

Date: 24.09.2023 Duration: 3 hours

- Note:
1. This question paper contains two parts A and B.
  2. Part A is compulsory which carries 20 marks. Answer all questions in Part A.
  3. Part B consists of 5 Units. Answer any one full question from each unit.
  4. Each question carries 10 marks and may have a, b, c, d as sub questions.

Part-A

All the following questions carry equal marks

(10x2M=20 Marks)

- 1 Define Modulation.
- 2 What is ring modulator?
- 3 Categorize and compare all AM techniques.
- 4 Write the applications of different AM systems.
- 5 Calculate the average power of an FM signal.
- 6 Write the disadvantages of FM systems?
- 7 Classify the sources of Noise?
- 8 Define Shot noise?
- 9 What are image frequencies?
- 10 What is the need for AGC circuit?

Part-B

Answer All the following questions.

(5X10M=50Marks)

- 11.a. Explain switching modulator. 6
  - b. A standard AM transmission, sinusoidally modulated to depth of 40%, produces side band frequencies of 6.824 and 6.854 MHz. The amplitude of each sideband frequency is 50v. Determine the amplitude and frequency of the carrier? 4
- OR
12. Use the envelope detector to generate AM wave with a neat sketch. 10
  13. List out the various methods used to generate SSB signals with neat sketches. 10
- OR
14. Evaluate the expression for VSB along with need of VSB modulation. 10
  15. Derive the expression for FM signal from fundamentals and differentiate narrow band FM and wide band FM. 10
- OR
16. Generate the narrow band and wide band FM With a neat block diagram. 10
  17. Draw the block diagram of FM demodulator and explain the effect of noise in detail and compare the performance of AM and FM in the presence of noise. 10

- OR
- 18 Justify that the pre-emphasis and de-emphasis in FM is used to reduce the noise ,With a neat block diagram. 10
- 19 Design the block diagram of a super heterodyne receiver and explain its operation? What are the advantages of this receiver? 10
- OR
- 20 Generate a simple AGC circuit and explain the different types. 10