



	OR
16	(a) Explain the working of Half wave rectifier with inductor filter with a neat circuit diagram. [5M] (b) Explain about capacitor filter using Full wave rectifier and calculate its ripple factor. [5M]
17	Explain the input and output characteristics of CE configured transistor circuit with a neat circuit diagram. [10M]
	OR
18	(a) Define α , β and Y . Write relation between α , β and Y [5M] (b) Determine the operating point for a fixed bias circuit whose $V_{cc}=12V$, $R_c=4K\Omega$, $R_b=930K\Omega$, $\beta=50$ for a silicon transistor. [5M]
19	(a) Explain the operation of JFET with the help of neat diagram. [5M] (b) Explain the FET parameters. Write the relation between Drain resistance, Trans-conductance and amplification factor. [5M]
	OR
20	Explain the construction and operation of a Depletion MOSFET and draw its characteristics. [10M]