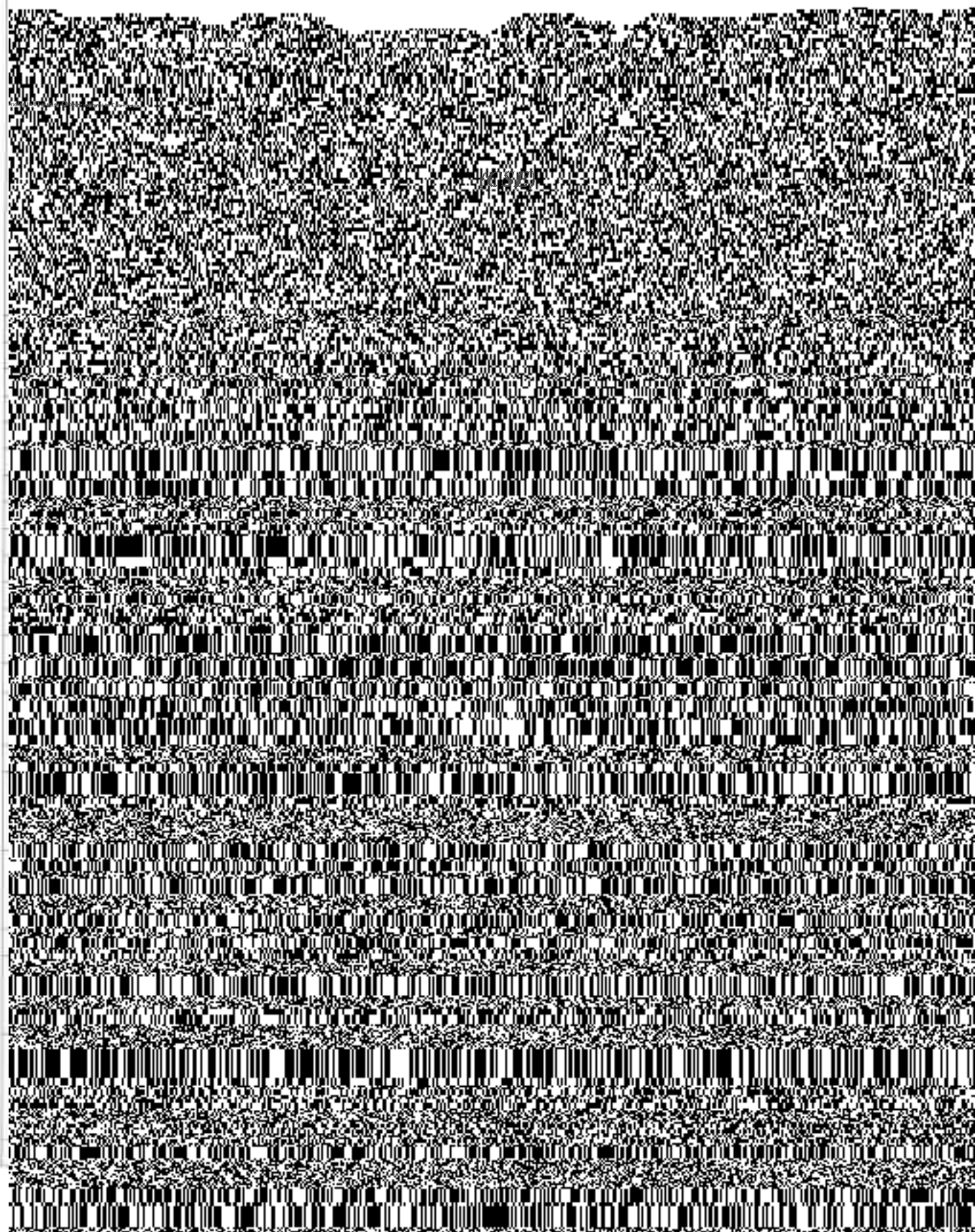




16	A) Explain Booth's algorithm with its theoretical basis. B) Represent two n-bit unsigned numbers multiplications with a series of n/2-bit multiplications.	5M 5M	3	L2
17	Explain Virtual memory with neat diagrams and examples.	10M	4	L2
	OR			
18	Explain the following a) Memory mapped I/O b) I/O Registers c) Hardware Interrupts and Programmed I/O	10M	4	L2
19	A) Explain how Flynn classified the processors into different streams by giving an example for each stream. B) Explain tightly coupled and loosely coupled systems with suitable examples.	5M 5M	5	L2
	OR			
20	A) Draw and explain a typical functional structure of SIMD array processor. B) What is an array processor?	5M 5M	5	L2