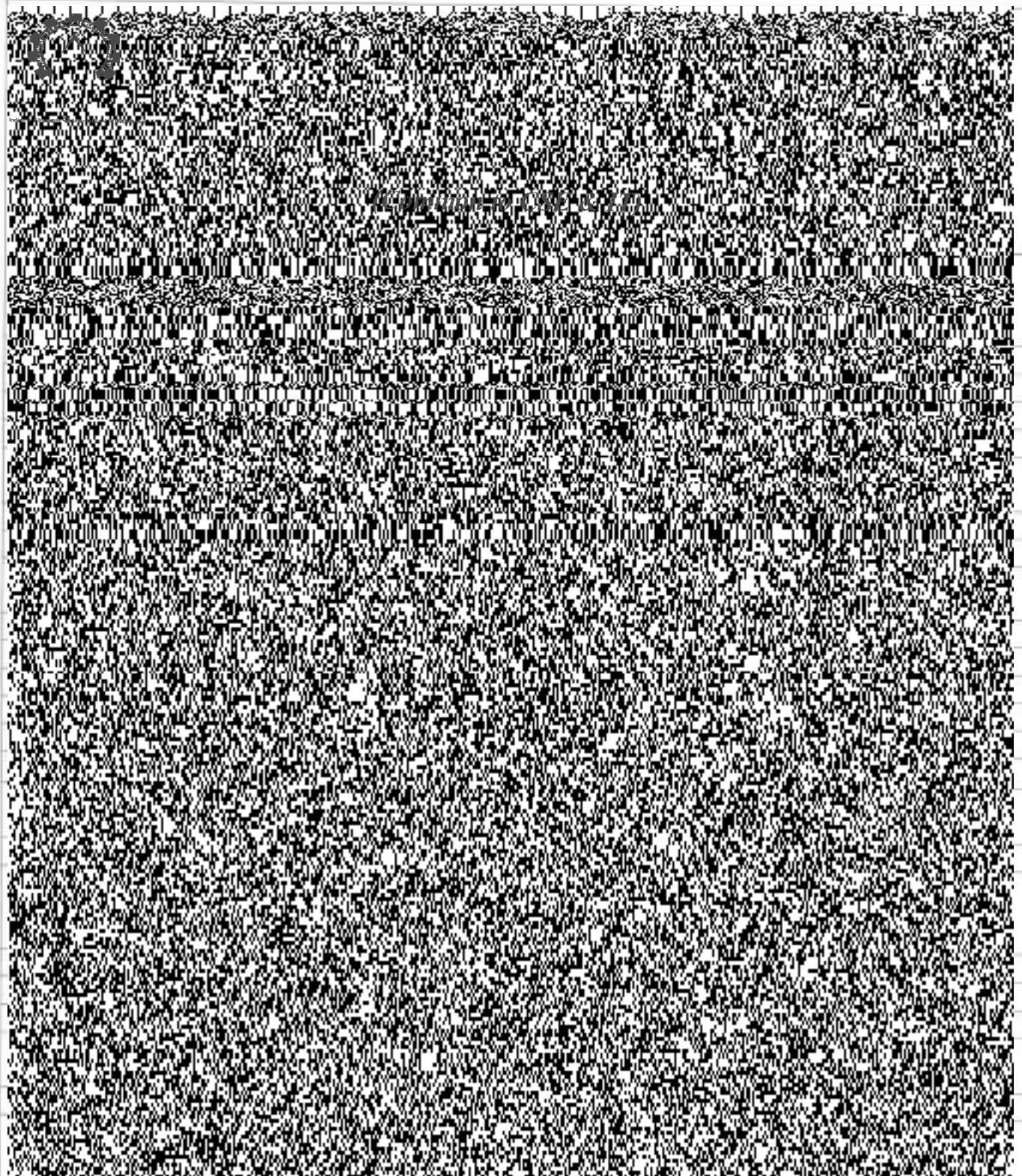




17	Write about the memory management hardware.	10M	4	L2
	OR			
18	Explain about the cache memory.	10M	4	L2
19	Write about inter processor communication and Synchronization.	10M	5	L2
	OR			
20	Discuss about architecture pipeline and instruction pipeline.	10M	5	L2