



R18 Regulation

Subject code:2P4DD

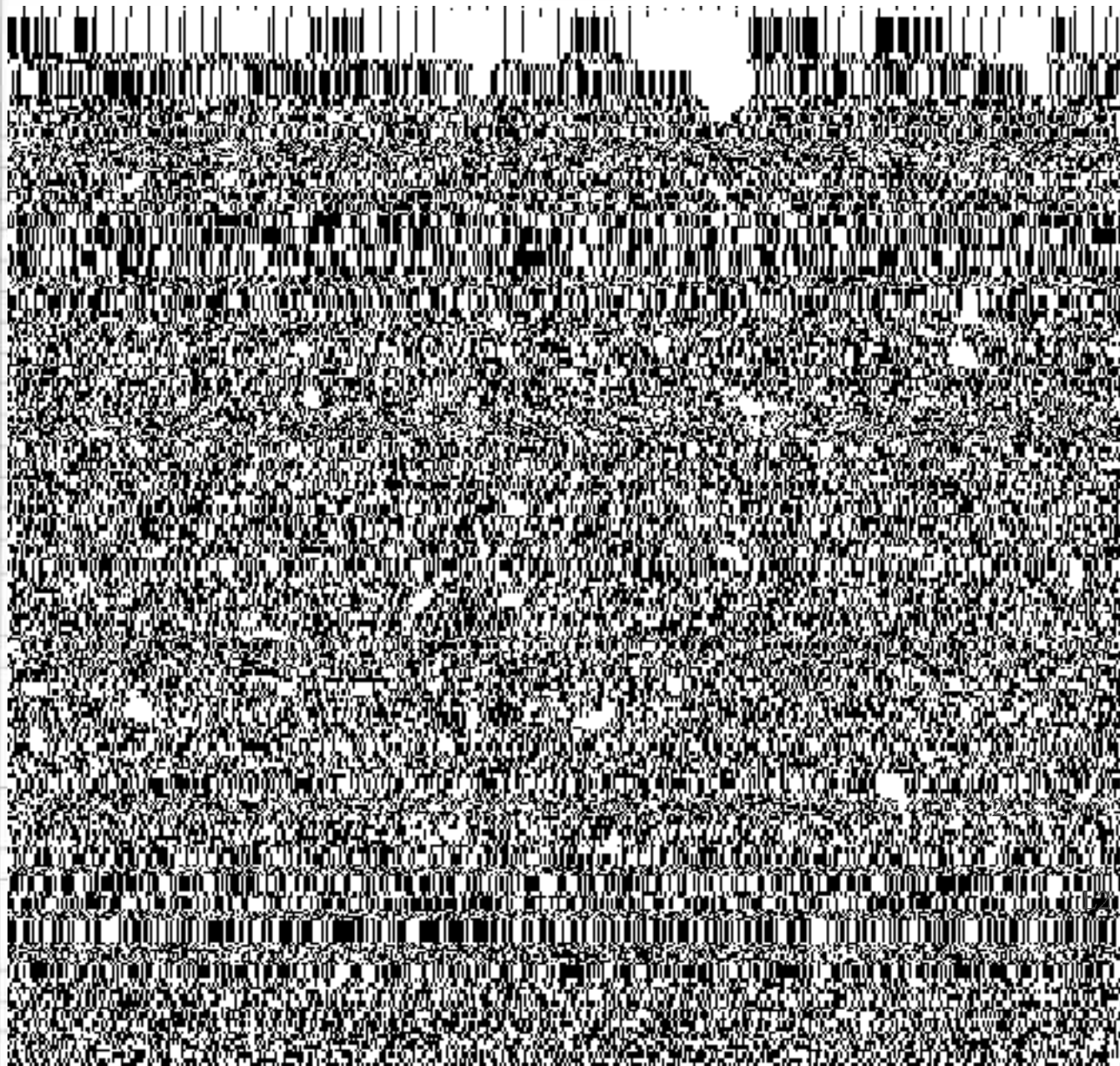
TKR COLLEGE OF ENGINEERING AND TECHNOLOGY

(Autonomous, Accredited by NAAC with 'A+' Grade)

B.Tech IV Semester Supplementary Examinations, April 2025

LINEAR & DIGITAL IC APPLICATIONS

(ECE)



17	a) Design Binary to Excess 3 code converter. b) Draw and explain the block diagram of n-bit parallel binary adder/subtractor.	5M 5M	4	L2
	OR			
18	Explain with neat diagram interfacing of TTL gate driving CMOS and CMOS driving TTL Gates.	10M	4	L2
19	Differentiate between ripple counter and synchronous counter? Design a 3- bit synchronous counter.	10M	5	L2
	OR			
20	a) Design and implement 4-bit synchronous down counter using IC. b) Explain the functional behavior of Static RAM cell? Show the internal structure of 8×4 static RAM.	5M 5M	5	L2